

# LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
1	2.02.15	Introduction to Computer Architecture	1	CR		
2	3.02.15	Number Representation Fixed Point	1	CR		
3	5.02.15	Floating point Representation	1	CR		
4	6.02.15	Error Detection codes	1	CR		
5	9.02.15	Error correction codes	1	CR		
6	10.02.15	Computer types & Classification	1	CR		
7	12.02.15	RTL, Bus & memory transfer	1	CR		
8	13.02.15	Microoperation Arithmetic	1	CR		
9	16.02.15	Logic & Shift microoperation	1	CR		
10	17.02.15	ALU Unit	1	CR		
11	19.02.15	Addition of Signed number	11	CR		
12	20.02.15	Subtraction of Signed number	11	CR		
13	23.02.15	Look ahead carry adder	11	CR		
14	24.02.15	Signed multiplication	11	CR		
15	26.02.15	Unsigned multiplication	11	CR		
16	27.02.15	Array multiplier	11	CR		
17	16.03.15	Division operation	11	CR		
18	17.03.15					
19	19.03.15	Floating point arithmetic operation	11	CR		
20	20.03.15					
21	23.03.15	processor organization	11	CR		
22	24.03.15					

# LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
23	26.03.15	Addressing mode	II	CR		
24	27.03.15	Instruction format	II	CR		
25 26	30.03.15 31.03.15	Instruction cycle	II	CR		
27	2.04.15	Basic concept memory Hierarchy	III	CR		
28	3.04.15	Semiconductor memories RAM	III	CR		
29 30	6.04.15 7.04.15	2D, 2.5D memory organization	III	CR		
31	9.04.15	ROM	III	CR		
32 33	10.04.15 13.04.15	Cache memory	III	CR		
34	14.04.15	Auxiliary memory	III	CR		
35 36	16.04.15 17.04.15	Virtual memory	III	CR		
37	20.04.15	Peripheral Devices	IV	CR		
38	21.04.15	I/O interface	IV	CR		
39 40	23.04.15 27.04.15	Mode of data transfer. Programmed I/O	IV	CR		
41	27.04.15	Interrupt initiated I/O	IV	CR		
42 43	28.04.15 30.04.15	Direct memory Access	IV	CR		
44	1.5.15	Synchronous communication	IV	CR		
45 46	18.5.15 19.5.15	Asynchronous communication	IV	CR		
47	21.5.15	Input output Processor	IV	CR		



## LESSON PLAN

[illegible]